A Radiation-Hardened Neuromorphic Imager with Self-Healing Spiking Pixels and Unified Spiking Neural Network for Space Robotics

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Abstract

A radiation-hardened neuromorphic imager prototype is developed for space exploration, featuring a fully spike-based neuromorphic vision system architecture, in-pixel self-healing against radiation-induced damage, and integrated unified spiking neural network (USNN) with adaptive neurons and synapses and contrast enhancement at low-contrast conditions. Self-healing reduces dark current by 6.25× at 14kGy cumulative dose, recovering recognition accuracy by 27.8%. USNN consumes 0.0529 pJ/SOP at 5,000 events/s.

Introduction

Space-grade ICs are in constant demand in view of the continuous interest in space exploration [1]-[8], where the operating environment challenges silicon systems (e.g., radiation from cosmic rays induces damage and faults, extreme lighting conditions limit the harvested power from solar panels). Such harsh conditions mandate high radiation tolerance, low power consumption, and resilient processing in space ICs (Fig.1). For space image sensors, radiation causes total ionizing dose (TID) and displacement damage dose (DDD) in CMOS pixels [9]-[10], resulting in faulty responses and hot pixels with high dark current [11]-[14]. Voltage compensation has been used to mitigate these effects but requires high voltages (9-30V) and significant power (>0.2W) [11]. While silicon carbide (4H-SiC) photodiodes (PDs) offer strong radiation tolerance, their quantum efficiency in the visible range is limited [14]. Techniques like gate-overlap PDs and enclosed layout transistors [11]-[13] can enhance radiation tolerance but fail to recover damaged pixels, thus not extending the actual pixel lifetime. Annealing can effectively repair radiation-induced lattice damage, but heating the full sensor incurs significant power [15]. Furthermore, conventional pixel readouts [11]-[14], relying on analog sampling and conversion, are susceptible to transient faults from radiation-induced charge fluctuations and signal distortions, while also lacking the processing capability required for image enhancement and advanced space exploration tasks. Imagers incorporating CNNs [16] or BNNs [17] add intelligence but remain susceptible to radiation in space.

Proposed Neuromorphic Imager

This work presents a radiation-hardened neuromorphic imager for space robotics (Fig.2), featuring: 1) a fully spike-based vision system, which eliminates the need for ADCs, enhances resilience to radiation-induced faults, and achieves state-of-the-art energy efficiency; 2) radiation-hardened pixel array with in-pixel localized annealing, enabling self-healing against radiation damage; 3) integrated unified spiking neural network (USNN) incorporating adaptive excitatory and inhibitory (E/I) neurons and synapses for space object recognition and spatialization; and 4) spike-based time-domain exponential (STE) contrast enhancement for low-light conditions. Key feature demonstrations are shown in Fig.3.

The system (Fig.2) consists of a 73×73-pixel array, a USNN, pixel readout, controller, and an SPI interface. The pixel array generates spike signals with frequencies representing light intensity, which are directly fed into the USNN with three task-specific output heads for terrain recognition, object positioning, and focus state analysis. Each USNN layer processes spikes using digital counters, multiplying binary weights (-1, +1) and firing output spikes to the next layer. The distributed nature of spikes offers inherent redundancy and resilience [18]-[22],

making our system more fault-tolerant than traditional sampling-based systems.

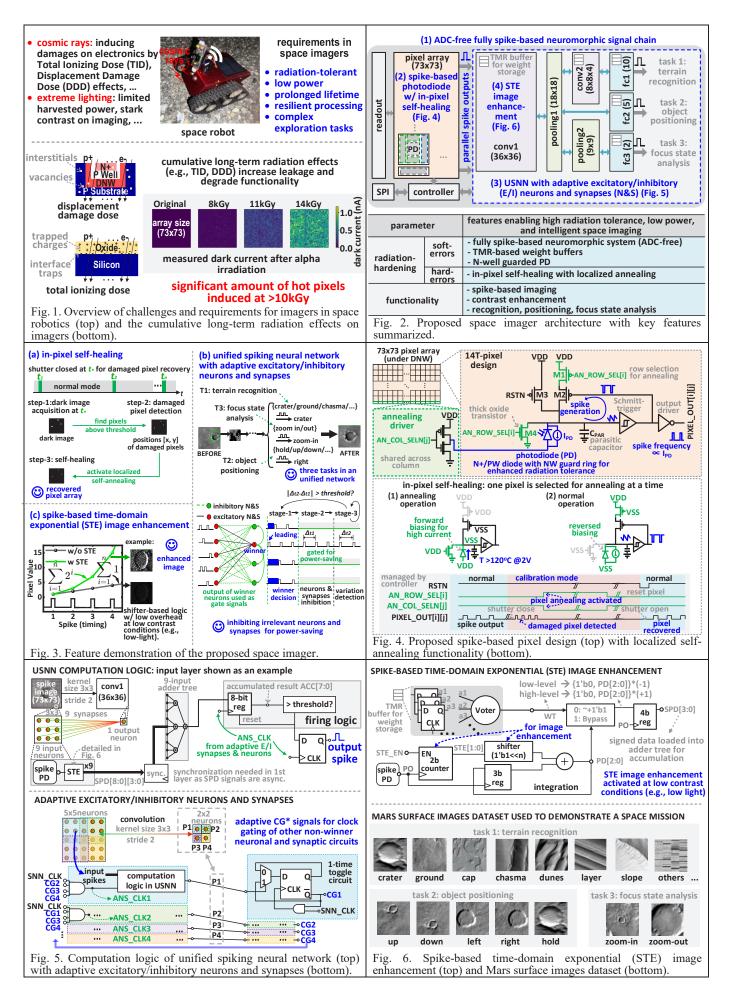
Triple module redundancy (TMR) registers are used for weight storage, but TMR proves less effective for CMOS pixels, which are susceptible to cumulative long-term radiation damage. Accelerated alpha irradiation tests reveal significant hot pixels at doses exceeding 10kGy, with dark current increases of ~10,000× (Fig.1, bottom). To address this, in-pixel annealing is introduced to enable localized thermal recovery of silicon's electrical properties. Fig.4 shows the pixel design integrating spike generation and self-annealing. The PD is an N+/P- well diode with an extra N-well guard ring for enhanced radiation tolerance. Spike generation is achieved through a positive feedback loop, where photocurrent from reversedbiased PD alternately charges and discharges the Schmitttrigger buffer, producing output pulses with frequencies proportional to light intensity [18]. In self-annealing, the PD is forward biased by the column driver, creating a local hot spot (>120°C @ 2V) for annealing with minimized power overhead.

USNN (Fig.5, top) begins with STE modules for synaptic integration. A synchronizer aligns asynchronous pixel spikes before aggregation, firing spikes to subsequent layers when thresholds are exceeded. To enhance energy efficiency, adaptive E/I neurons and synapses inhibit irrelevant neurons and synapses using clock gating (Fig.5, bottom). For low-light conditions commonly encountered in space exploration, STE contrast enhancement exponentially accumulates pixel spikes during synaptic integration via shifters (Fig.6, top and Fig.3(c)).

Measurement Results

The proposed 180nm design (Fig.10) exhibits excellent spike frequency linearity with light intensity, achieving an R² value of 99.7% (Fig.7, top). Under low-light conditions (~100 lux), STE contrast enhancement improves inference accuracy by 57.8%-63.1% and inference speed by 3.3×. The USNN operates at 200 kHz to match the highest spike frequency and consumes 59 μW at 0.6V during recognition and spatialization tasks for the Mars surface images dataset (Fig.6, bottom). Adaptive E/I neurons and synapses reduce USNN power consumption by 31.2% with 61.83% of synapses inhibited, achieving a system energy (including pixel array) of 0.0529 pJ/SOP (18.9 TSOPS/W), marking a 51-to-639× improvement over previous SOTA imagers (Table.I). Accelerated alpha irradiation experiment is performed using Americium-241 with a radioactivity of 4 Mbq (significantly exceeding typical space conditions, Fig.8). During in-pixel localized annealing, the local power density ranges from 0.11 to 0.47 mW/µm² at 1.5-2.5V, reaching temperatures above 120°C at a typical voltage of 2.0V (Fig.8, bottom). Damaged pixel recovery was evaluated under varying radiation doses, demonstrating both quick recovery within 3 minutes and repeated recovery (Fig.9, left). In repeated recovery, after four rounds of accelerated irradiation and annealing, self-healing pixels show a dark current increase rate that is 6.25× slower than untreated pixels, effectively extending the device lifespan. As cumulative doses increase, the number of damaged pixels rises, degrading USNN accuracy. By applying self-healing, the USNN accuracy is recovered by 27.8% under 14kGy irradiation (Fig.9, right). Acknowledgements This work was supported in part by the Nat'l

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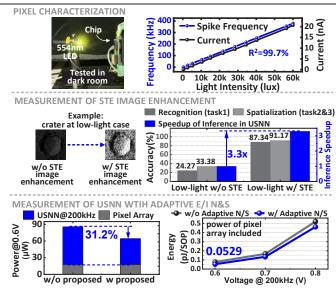
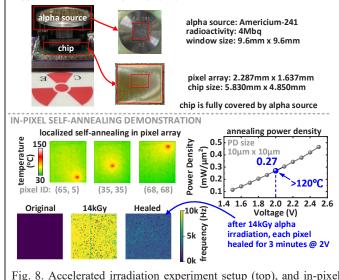
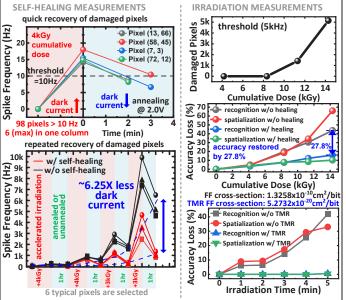


Fig. 7. Measurements: pixel characterization (top), STE enhancement (middle), USNN with adaptive E/I neurons and synapses (bottom).



ACCELERATED IRRADIATION TEST SETUP

Fig. 8. Accelerated irradiation experiment setup (top), and in-pixel annealing demonstration (bottom).



F

9k - w/c 8k - osa		rk ent	TMR FF CI (% 40	Recognition w/c Spatialization w Recognition w/ Spatialization w	8 10 12 14 Dose (kGy) .3258x10 ⁻¹⁰ cm ² /bit .2732x10 ⁻²⁰ cm ² /bit or TMR				
Fig. 9. Me	ig. 9. Measurement results of accelerated irradiation experiments.								
TABLE I. Comparison with state-of-the-art imagers									
1 F	ABLE I. Co	omparison v	vith state-of	-the-art im	agers	1			
17	ABLE I. Co [16] JSSC2023	*	vith state-of	-the-art im:	agers this work	إ			
technology		*							
technology	[16] JSSC2023	[17] ISSCC2024	[11] commercial	[13] TNS2015	this work				
technology	[16] JSSC2023 180nm CMOS	[17] ISSCC2024 180nm CMOS	[11] commercial	[13] TNS2015 180nm CMOS	this work 180nm CMOS imager + SNN 0.6-0.8 (normal) 1.5-2.5 (self-healing)				
technology nip architecture	[16] JSSC2023 180nm CMOS imager + CNN	[17] ISSCC2024 180nm CMOS imager + BNN analog: 0.35	CIS imager 3.3 (normal),	[13] TNS2015 180nm CMOS imager analog: 3.3	this work 180nm CMOS imager + SNN 0.6-0.8 (normal)				
technology nip architecture supply voltage (V)	[16] JSSC2023 180nm CMOS imager + CNN 0.8	[17] ISSCC2024 180nm CMOS imager + BNN analog: 0.35 digital: 1.1	CIS imager 3.3 (normal), -9~-30 (V comp.)	[13] TNS2015 180nm CMOS imager analog: 3.3 digital: 1.8	this work 180nm CMOS imager + SNN 0.6-0.8 (normal) 1.5-2.5 (self-healing)				

	[16] JSSC2023	[17] ISSCC2024	[11] commercial	[13] TNS2015	this work
technology	180nm CMOS	180nm CMOS	CIS	180nm CMOS	180nm CMOS
chip architecture	imager + CNN	imager + BNN	imager	imager	imager + SNN
supply voltage (V)	0.8	analog: 0.35 digital: 1.1	3.3 (normal), -9~-30 (V comp.)	analog: 3.3 digital: 1.8	0.6-0.8 (normal) 1.5-2.5 (self-healing)
area (mm²)	5.368	25	> 75	not reported	28.275
pixel array	128x128	128x128	720x720	128x128	73x73
pixel size (um²)	7.6x7.6	20x20	12x12	10x10	22x21 (10x10 for PD)
pixel structure	4T PWM	6T PWM	not reported	3T	14T spike
power consumption	not reported	1.578~121.74mW	>0.2W	not reported	76.3µW @ 0.6V
image enhancement	No	No	No	No	spike-based time- domain exponential
pixel radiation hardening	-	-	high voltage compensation	gate-overlap PD, enclosed layout	in-pixel self-healing, NW-guarded PD
dark current increase @14kGy	-	-	not reported	>100x	~125x w/o healing ~20x w/ healing
neural network	CNN (3-layer)	optical + BNN (3-layer)	-	-	USNN (7-layer)
memory type	flip-flops	flip-flops			TMR flip-flops
frame/event rate	50~250 fps	1~32 fps	-	-	1~5,000 event/s @1~50k lux lighting
dataset (accuracy)	LFW / Kaggle Oregon Wildlife (93.6%)	MNIST (96.4%), Eyes Orientation (94.9%)	-	-	Mars Surface Images (89.77% recognition; 93.18% spatialization)
energy efficiency	-	0.367 TOPS/W	-	-	18.889 TSOPS/W
energy per operation	33.8 pJ/pixel/frame	-	-	-	0.0529 pJ/SOP

5829.76µm		400
- 1933939161639161616161616161616161616161	technology	180nm
SPI	chip architecture	Imager + SNN
Conv1 2287.305μm 15 15 15 15 15 15 15 15 15 15 15 15 15	chip size	5.83mm x 4.85mm (pixel array: 3.74mm²)
73 X 73 & Spiking Pixel & 50.	pixel array	73 x 73
73 X 73 33. 48 55. 16 5	pixel structure	14T spike
	power	76.3μW @ 0.6V
FC1 FC2 Conv2	image enhancement	spike-based time-domain exponential
USNN Accuracy VS. Time Step	radiation hardening of pixel	in-pixel self-healing via annealing (27mW/pixel @ 2.0V), NW-guarded PD
	radiation tolerance	allowing for repeated
© 60 /P	capability	recovery for pixels
200	neural network	unified SNN
75 60 60 45 30 Recognition Spatialization	precision	1-bit weight 4-bit activation
0 1 2 3 4 5 6 7 Time Step	dataset (accuracy)	Mars surface images (89.77% for recognition; 93.18% for spatialization)
The time step is the discrete interval of simulated time,	energy efficiency (TSOPS/W)	18.889 @ 0.6V
matching 1 clock cycle of the	radiation hardening of NN	TMR FF cross-section: 5.2732x10 ⁻²⁰ cm ² /bit
USNN's operating frequency.	energy	0.0529 pJ/SOP @ 0.6V

Fig. 10. Die micrograph and chip summary.

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